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Freescale Semiconductor, Inc.
Law Department
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MD: TX32/PL02
Austin, TX 78729
Telephone: (512) 996-6839
Facsimile: (512) 996-6854

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Date: October 5, 2005
To: Tuan N. Quach - 2826
Location: United States Patent and Trademark Office
Fax No.: (571) 273-8300
From: Daniel D. Hill - 35,895
Subject: 10/807,624- Helmut Brech

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MESSAGE:

Enclosed herewith, please find an ELECTION for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
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EXAMINER: Tuan N. Quach
GROUP ART UNIT: 2826
SERIAL NO.: 10/807,624
FILED: 3/24/2004
INVENTOR: HELMUT BRECH

OCT 05 2005

DUCKET NO. SC131912F

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Helmut Brech GROUP ART UNIT: 2826
APPLN. NO.: 10/807,624 EXAMINER: Tuan N. Quach
FILED: 3/24/2004
TITLE: TRANSISTOR WITH REDUCED GATE-TO-SOURCE
CAPACITANCE AND METHOD THEREFOR

Certificate of Transmission under 37 CFR 1.8

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ELECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated 9/16/2005, and Examiner's comments with regard thereto, please enter the following election in the above-entitled application, without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079.